



PATENT APPLICATION
Attorney Doc. No. 2705-220

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Law, et al.

Serial No. 10/056,102 Group Art Unit: 2662

Filing Date: January 23, 2002 Examiner: Not yet assigned

For: DIAL ACCESS STACK ARCHITECTURE

Box Fee Amendment
Assistant Commissioner for Patents
Washington, D.C. 20231

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PRELIMINARY AMENDMENT

IN THE SPECIFICATION

Please replace the paragraph beginning at page 5, line 5 with the following rewritten paragraph:

A1
-- The system controller 24 manages and monitors the components in DASA stack 12 and also serves as the primary Network Timer Protocol (NTP) clock source for the members of the DASA stack 12. A modem 27 is used to send management data extracted from the DASA components to a central network manager (not shown). The system controller 24 is used for out of band access to all DASA components. The system controller features a triple redundant path to each component: primary interconnect, secondary interconnect, serial connection. --

Please replace the paragraph beginning at page 8, line 13 with the following rewritten paragraph:

A2
-- The network switches 18 and 20 are designed to connect together a large number of devices at the same time. Therefore, the DASA is easily scaled to add any number of NASs 32 or routing engines 22 to the DASA 12. The interconnect switches 18 and 20 in one embodiment use LAN interfaces that are the common interface used by the different NASs 32 and routing engines 22. The LAN interfaces allow almost any commercially available NAS

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